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Box PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, DC 20231

New U.S. Patent Application

Title: METHOD OF MANUFACTURING  
SEMICONDUCTOR DEVICE

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Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 84 pages, including 1 independent claim and 19 claims total.
2. Drawings- 8 sheets of formal drawings containing 19 figures

3. The filing fee is calculated as follows:

Basic Application Filing Fee					\$740	\$ \$740.00
	Number of Claims		Basic	Extra Claims		
Total Claims	19	-	20		x \$18	
Independent Claims	1	-	3		x \$84	
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$280	
Subtotal						\$ 740.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 740.00

4. A check for \$740.00 is enclosed. The fee includes \$740.00 filing fee.
5. Applicants claim the right to priority based on Japanese Application Nos. 2000-320680, 2001-098182, 2001-098183 and 2001-321448, filed October 20, 2000, March 30, 2001, March 30, 2001 and October 19, 2001, respectively.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Please address all correspondence with respect to this application to:

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Please accord this application an application number and filing date and record and return the Assignment to the undersigned.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By: 

Richard V. Burgujian  
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RVB/FPD/bl  
Enclosures

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